

## Patent Abstracts of Japan

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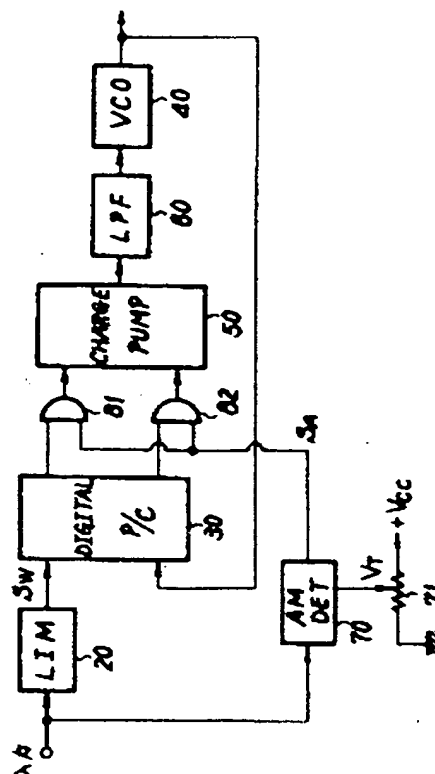
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TITLE : PLL



**ABSTRACT :** PURPOSE: To prevent the lock failure, by detecting whether the amplitude of the AM-modulated carrier is a fixed value or more or not and by stopping the phase comparison function if it is a fixed value or less, in respect to the PLL of the AM stereo receiver.

CONSTITUTION: The input AM signal is made into a square wave signal by limiter 20 and is supplied to one input of digital phase comparator 30, and simultaneously, the input signal is supplied to amplitude detecting circuit 70 also, and threshold voltage VT is obtained from the regulator of variable resistor 71, and signal SA to output the high-level output for a fixed value or more of the amplitude of carrier CA and output the low-level output for the fixed value or less is generated. When signal SA becomes low-level, gates 81 and 82 are closed to prevent the input from the digital phase comparator to charge pump 50, and the phase comparison function is stopped to set pump 50 to the floating state, and the output voltage of low-pass filter 60 is kept in the preceding value, thus preventing lock failure.

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